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FORM JUN 0 5 2006		Application Number		10/715,267			
		Filing Date		November 17, 2003			
		First Named Inven	ned Inventor		Nathan R. Brown		
		Art Unit		1763			
		Examiner Name		S. Macarthur			
Total Number of Pages in This Submission		Attorney Docket N	umber	2269-4375.	1US (99-1029.01US)		
ENCLOSURES (check all that apply)							
Fee Transmittal Form	☐ Drawing(s			After Allowance Communication to TC			
Fee Attached	Licensing	Licensing-related Papers		Appeal Communication to Board of Appeals and Interferences			
Response to Final Office Action dated April 5, 2006				Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)			
After Final		Petition to Convert to a Provisional Application			Proprietary Information		
Affidavits/declaration(s)		Power of Attorney, Revocation Change of Correspondence Address			Status Letter		
Extension of Time Request	Terminal		Other Enclosure(s) (please identify below):				
Express Abandonment Request		for Refund ber of CD(s)					
Supplemental Information Disclosure Statement; PTO/SB/08/ Check no. 9611 in the amount of \$180.00	<u>, </u>	☐ Landscape Table on CD					
Certified Copy of Priority Document(s)							
Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR1.52 or 1.53	with any docu Deposit Acco	The Commissioner is authorized to charge any additional fees required but not submitted with any document or request requiring fee payment under 37 C.F.R. §§ 1.16 AND 1.17 TO Deposit Account 20-1469 during pendency of this application.					
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT							
Firm TraskBritt, P.C.							
Signature	Drow-						
Printed Name Brick G. Power							
Date June 1, 2006		Reg. No.		38,581			
CERTIFICATE OF TRANSMISSION/MAILING							
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Signature							
Typed or printed name Erika Gandre				Date	June 1, 2006		

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nathan R. Brown

Serial No.: 10/715,267

Filed: November 17, 2003

For: METHODS FOR POLISHING SEMICONDUCTOR DEVICE STRUCTURES BY DIFFERENTIALLY APPLYING PRESSURE TO SUBSTRATES THAT CARRY THE SEMICONDUCTOR DEVICE STRUCTURES

Confirmation No.: 4590

Examiner: S. Macarthur

Group Art Unit: 1763

Attorney Docket No.: 2269-4375.1US

(99-1029.01US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

This Response follows the final Office Action of April 5, 2006, the three-month shortened statutory period for response to which expires on July 5, 2006. This Response is being submitted within two months of the mailing date of the final Office Action.